

Fig. 1a

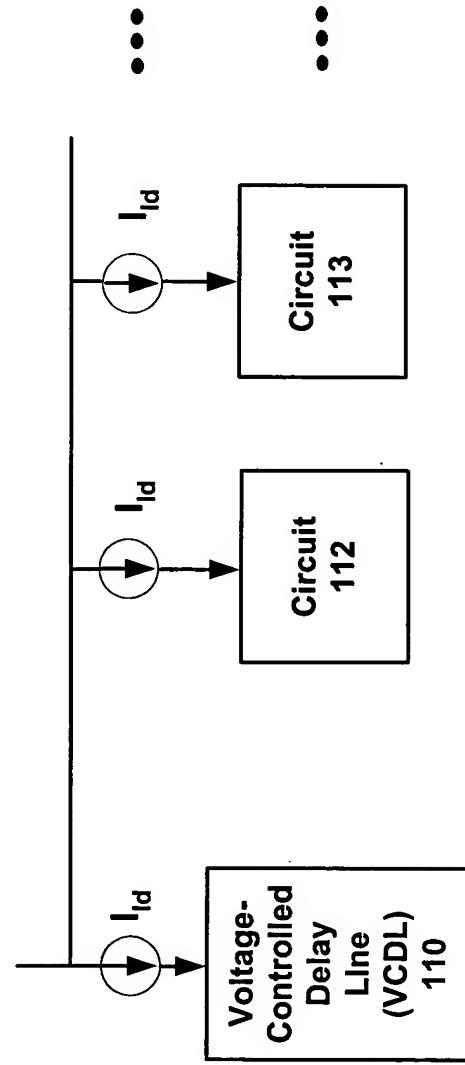


Fig. 1b

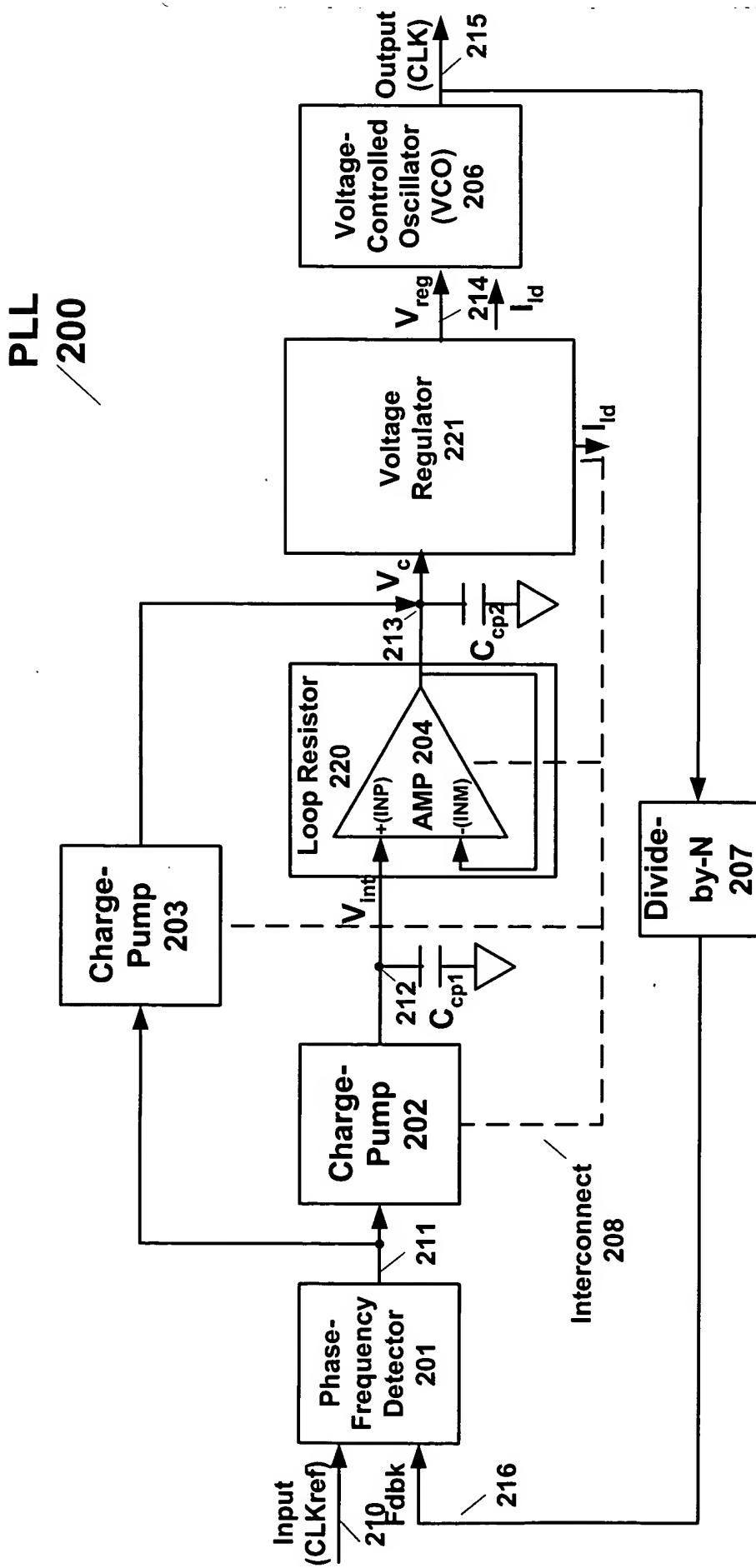


Fig. 2

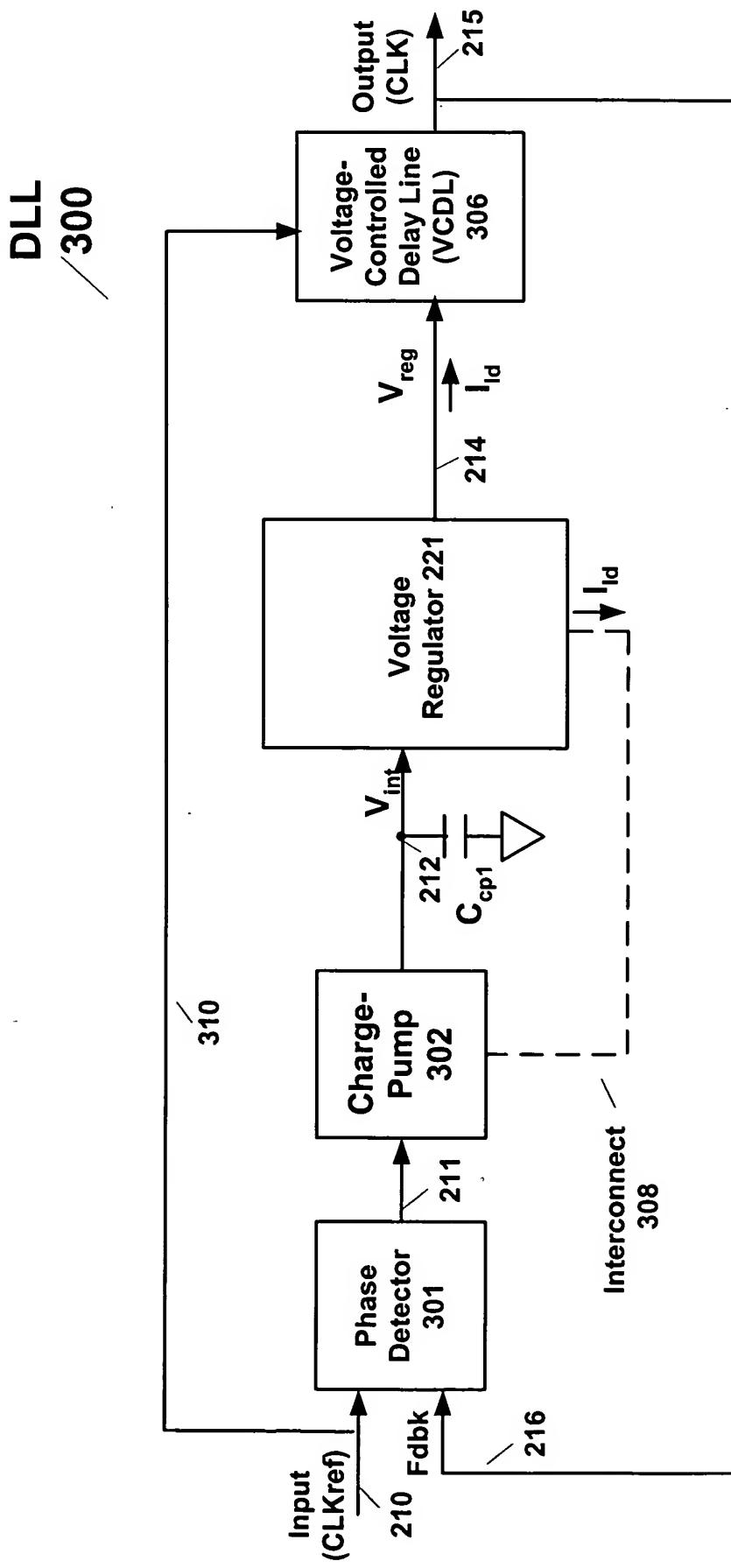


Fig. 3

Title: Regulated Adaptive-Bandwidth PLL/DLL Using Self-Biasing Current From A VCO/VCDL
Applicants: Xudong Shi, et al. Docket: RAMB-01015US1
Appl. No.: Unknown Atty: Kirk J. DeNiro, Esq.
Filing Date: Herewith Phone: (415) 369-9660
Express Mail No.: EV 391866921 US

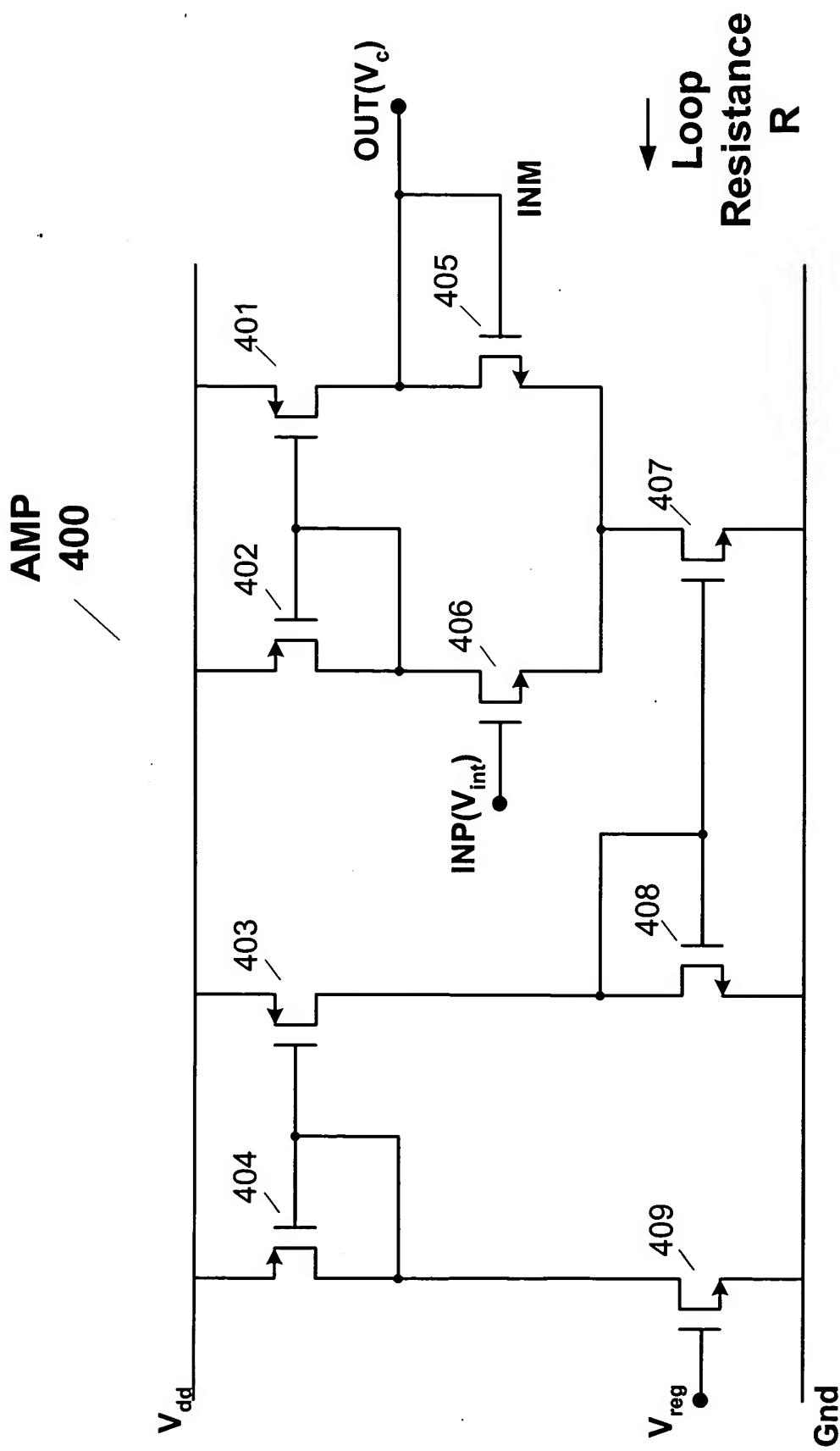


Fig. 4(Prior Art)

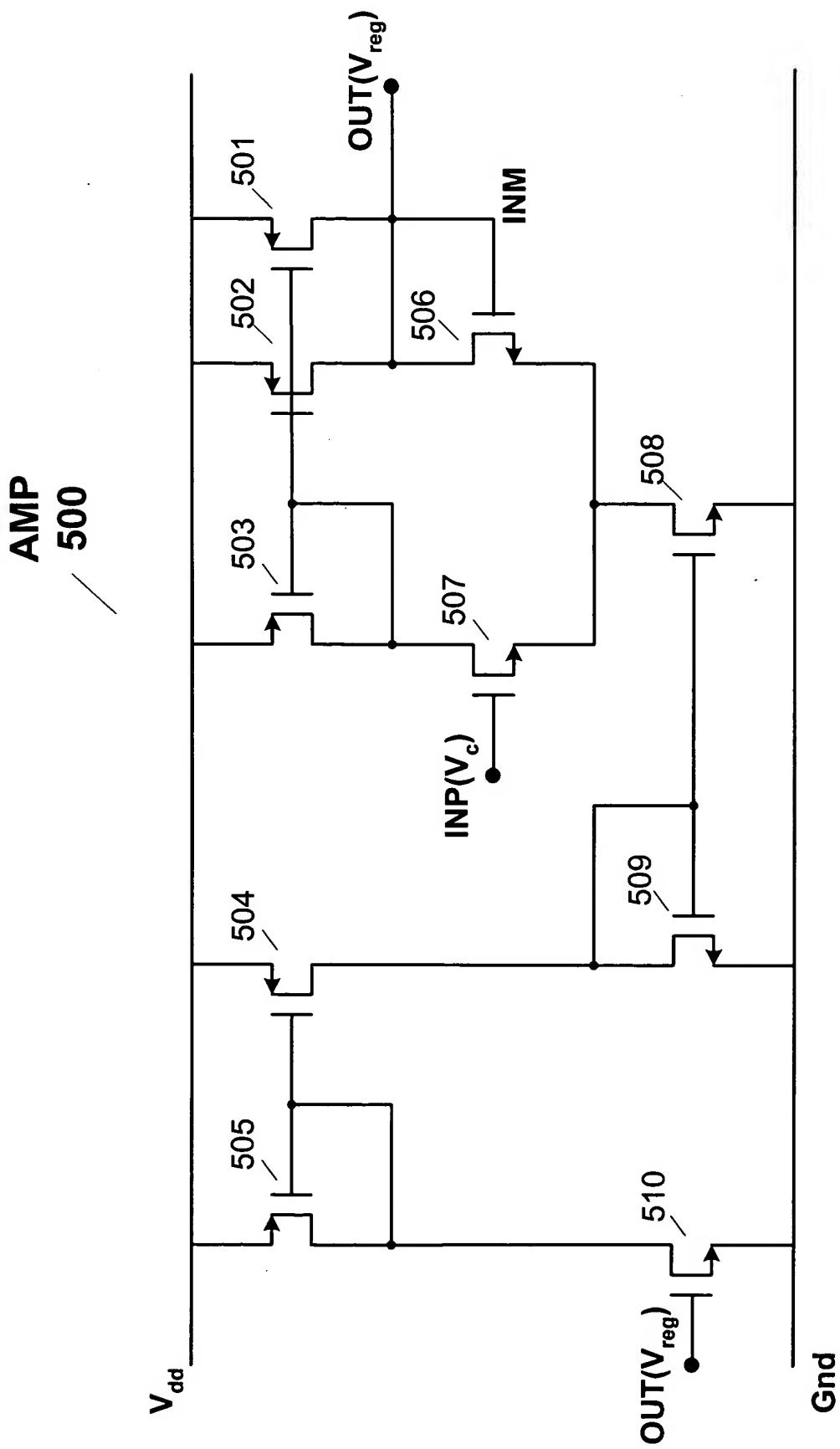


Fig. 5(Prior Art)

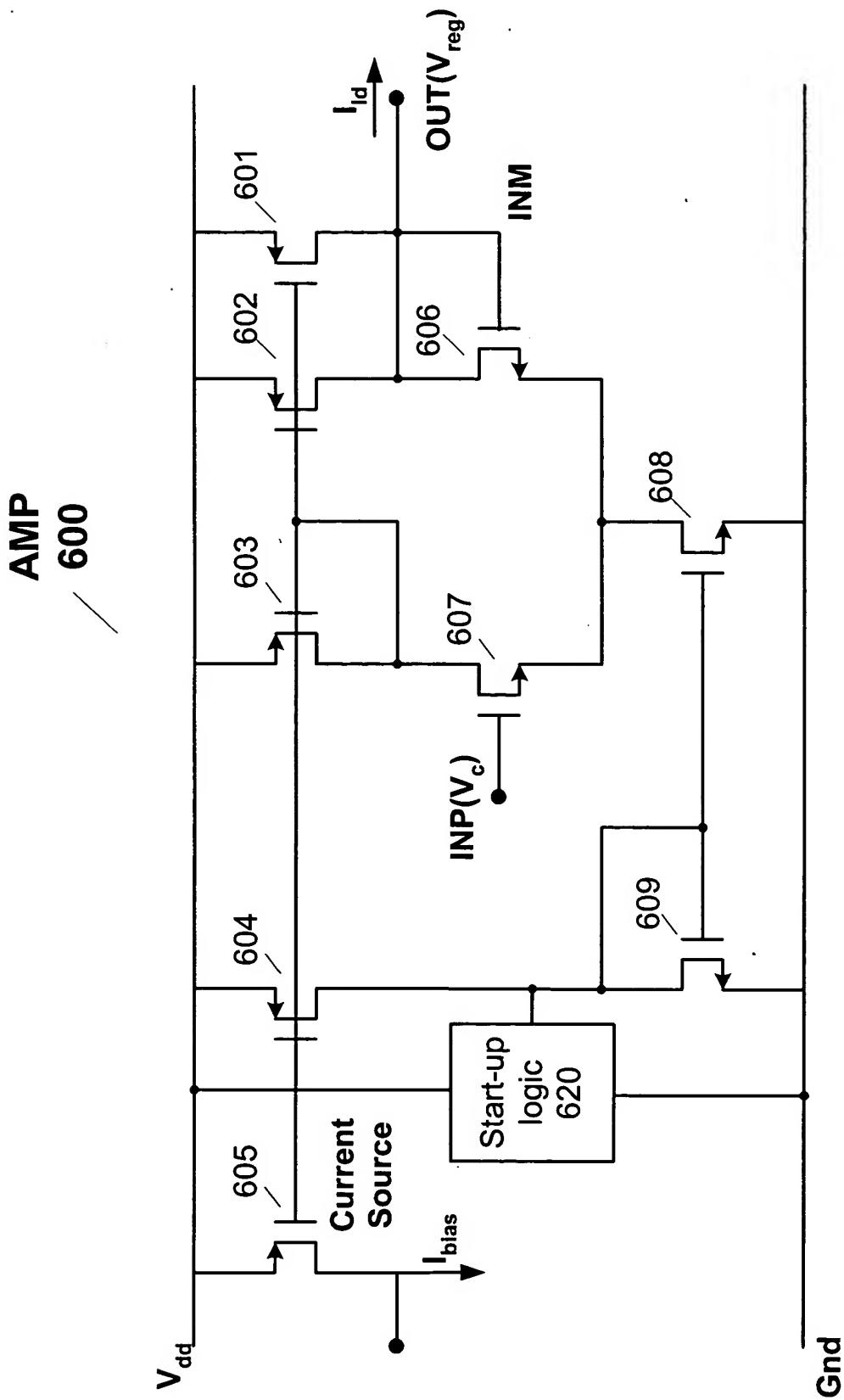


Fig. 6

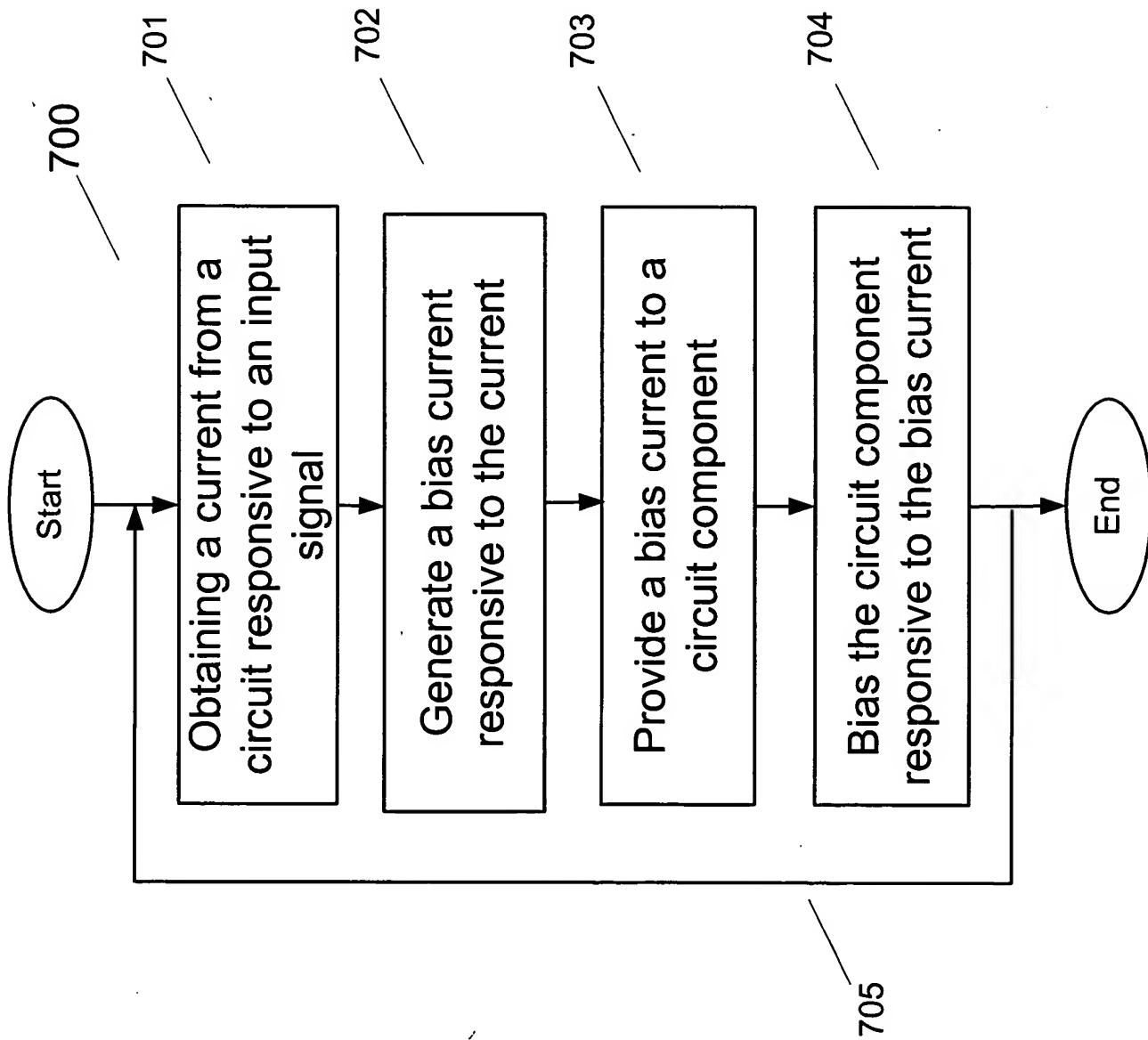


Fig. 7